UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 7,036,064 B1

APPLICATION NO.: 09/872212

DATED : April 25, 2006 INVENTOR(S) : Kebichi et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, line 21, "(n-11)" should be --(n-1)--.

Column 9, line 46, "Inn interleaved" should be --In interleaved--.

Column 10, line 12, "initialize and ran" should be --initialize and run--.

Column 10, line 47, "Inn the case" should be -- In the case--.

Column 12, line 46 "tool is then ran" should be --tool is then run--.

Column 12, line 42, "ram 116x16" should be --ram 16x16--.

Column 16, line 42, "Inn this embodiment," should be -- In this embodiment,--.

Signed and Sealed this

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Twenty-fourth Day of July, 2007

JON W. DUDAS
Director of the United States Patent and Trademark Office